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BIBDATASHEET

CONFIRMATION NO. 2011

Bib Data Sheet

SERIAL NUMBER 10/652,805	FILING DATE 08/29/2003 RULE	CLASS 438	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. NSC1P205D2
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** CONTINUING DATA *****

This application is a DIV of 09/947,210 09/04/2001 PAT 6,642,613
 which is a CIP of 09/568,558 05/09/2000 PAT 6,707,140
 and is a CIP of 09/713,367 11/14/2000 PAT 6,497,518
 and claims benefit of 60/331,377 08/03/2001

405 D-M-L

** FOREIGN APPLICATIONS *****

None D-M-L

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 12/04/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>[Signature]</i> Examiner's Signature	CA	9	22	2
<i>[Initials]</i> Initials				

ADDRESS

022434

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TITLE

Techniques for joining an opto-electronic module to a semiconductor package

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees
RECEIVED	No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> 1.16 Fees (Filing)
786	No. _____ for following:	<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
		<input type="checkbox"/> 1.18 Fees (Issue)
		<input type="checkbox"/> Other _____